

Commissioner for Patents

Amendment dated January 24, 2005

Response to Office Action dated September 22, 2004

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Serial No.: 10/054017

Art Unit: 2113

Examiner: Baderman

Docket No.: RPS9 2001 0090 US1

Amendments to the Specification:

Paragraph beginning on page 7, line 1:

Error detect signal 124 provides an input to error logging unit 124. Error logging unit 124 is configured to document an internal error by capturing the identity of the offending processor. Because the error detection logic is not resident on the processor ~~(bus and)~~ bus, it does not have visibility to the internal registers of the processors. In most cases when a processor asserts IERR ~~[occurs]~~, the processor has experienced an internal fatal error rendering most of its information unusable.